# Study on High Step-up DC-DC Converter with High Gain Cell

## for PV Applications

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#### Abstract

Switching power converters for photovoltaic (PV) applications with high gain are rapidly expanding. To obtain higher voltage gain, low switch stress, low ripple, and cost-effective converters, researchers are creating several topologies. In this work, several High Gain (HG) cells are studied, and the performance of the cells when combined with quadratic boost converters is investigated. We compared the voltage gain, voltage stress and switch utilisation factor, and efficiency of each type of HG cell. The NL5 simulator is used for simulation. In a positive output HG cell, an effective HG cell is identified..

*Keywords*: DC-DC converter; Topology; High Gain(HG) cell; voltage gain; voltage stress.

#### 1. Introduction

The total installed grid connected solar power capacity in India is about 8 GW as of 2016. Indian government has planned to invest \$100 billion to expand the solar plants to 100 GW by 2022. To achieve high efficiency in this Renewable energy system (RES), the energy conversion system should improved by implementing novel and efficient topologies. The high voltage dc-dc converter is presented in the year 1997 with voltage multiplier, and it is

advantageous in weight and size compared to conventional switched mode regulator [1]. M. S. Makowski addressed the question relating to the number of circuit elements required to achieve the particular voltage gain [18].

Nomenclatur	re
HG	High Gain
PQBHGC	Positive output Quadratic Boost Converter with High Gain Cell
D	Duty cycle
Vo	Output voltage
Vg	Input voltage
VL	Inductor voltage
V <sub>C</sub>	Capacitor voltage
V <sub>CM</sub>	Multiplier capacitor voltage

For the last decade, many types of research are going on the high gain dc-dc converters. It can be realized by adding High Gain (HG) cell before the output of the converter. Three state switching cells combined with voltage multiplier cell to increase the output voltage. However, the converter is bulky due the presence of auto transformer [3][17]. Zero voltage switching and zero current switching are proposed for the high step-up dc-dc converter with voltage multiplier cell [16]. Leakage inductance in the active clamp coupled inductor based dc-dc converter affects the voltage gain of the converter. This drawback is overcome by combining coupled inductor and voltage multiplier cell which reduces the high pulse current in the voltage doubler cell [8].Modified interleaved boost converter is integrated with voltage multiplier module with dual coupled inductor to balance the primary currents [5].

The most predominately used HG cell are given in Table 1. Table 1 provides different connection of Diode-Capacitor (D-C) cell and Diode-Capacitor-Inductor (D-C-L) cell. Initially used voltage multiplier circuits are Villard, Heinrich Greinacher, Dickson and Cockcroft-Walton circuits to achieve high output voltage. The HGC-2 cell is combined with voltage lift circuit to obtain low voltage stress on the switches [14]. The HGC-3 cell is Dickson multiplier cell and, it is used to derive significant voltage dc-dc converter in [15][23]. In [12] Dickson cell is used along with switched coupled inductor to increase the voltage gain. Diode reverse recovery losses are alleviated in the converter proposed with interleaved three winding coupled inductor with voltage multiplier cell [7]. The HGC-3 cell is integrated with conventional boost converter, and its performance is thoroughly studied in [10]. Ultra step-up dc-dc converter is derived with the HGC-3 cell in [4]. In [10] multiphase dc-dc converter is combined with HGC-7 cell to reduce input current ripple and for proper current sharing in the components of the converters. It is also in the interleaved boost converter with alternating phase shift control scheme [13]. Chung-Ming Young et al, proposed transformerless high step-up dc-dc converter with Cockcroft-Walton (HGC-5) cell for small input dc system [2]. HGC-9 is proposed as the bidirectional dc-dc converter in [6]. HGC-1 and HGC-15 are proposed in [11] for increasing PV cell voltage. HGC-2, 4,11,12,13 are proposed as inverting and non-inverting cell to derive a family of single switch high step-up dc-dc converter [19]. HGC-8 and HGC-14 are presented as four terminals PWM switch to achieve reduced voltage stress in [20]. HGC-10 is a Marx generator and a dc-dc converter is presented on this principle to generate the higher voltage in [27]. HGC-6 is quasi z-source network, -and it is used to derive Step-up dc-dc converters by cascading quasi z-source cell [28].

This paper is organized as follows: Section 1 gives the introduction about the high gain dc-dc converter with voltage multiplier cell. Section 2 provides the general representation of dc-dc converter with HG cell. Section 3 provides the steady state analysis of PQBHGC. Section 4 gives the comparison of different HG cell. Section 5 concludes the work.

Table 1: Various Voltage multiplier cell in literature

	HIGH GAIN (HG) CELL	
$\begin{array}{c} c_{1*} \\ \hline \\ C_{M1} \\ \hline \\ C_{M2} \\ \hline \\ C_{M2} \\ \hline \\ C_{M2} \\ \hline \\ C_{M2} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \\ C_{2*} \\ \hline \\ C_{2*} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \\ C_{2*} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \end{array} \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \hline \end{array} \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \end{array} \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \end{array} \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \end{array} \\ \end{array} \\ \begin{array}{c} c_{2*} \\ C_{2*} \\ \end{array} \\ \end{array} \\ \end{array} \\ \end{array} $	$c_{1}$	$c_1$
C1.	$C_{1} \xrightarrow{C_{M2}} C_{2}$	C <sub>L</sub> C <sub>2</sub> C <sub>2</sub> C
$\begin{array}{c c} c_{1,+} & c_{2,-} \\ \hline \\ c_{2,-} & c_{2,-} \\ c_{2,$	$C_{1,-} \rightarrow D_{M1}$ $D_{M2}$	$c_1$ $ c_2$ $c_2$ $c_2$
С <sub>1</sub> . — С <sub>12</sub> НGC-4 [19][24]	$\begin{array}{c} C_1 \xrightarrow{ \\ C_{2+} \end{array} \xrightarrow{ \\ C_{2+} \end{array} \xrightarrow{ \\ C_{2-} \end{array}} \begin{array}{c} C_2  \\ C_2 \xrightarrow{ \\$	с <sub>ь</sub> с
$C_{1-} \xrightarrow{C_{M1}} D_{0} C_{2+}$	$C_{11}$ $C_{11}$ $C_{11}$ $C_{11}$ $C_{11}$ $C_{2+}$ $C_{2+}$	$C_{1-}$ $C_{2+}$ $C_{31}$ $C_{2+}$ $C_{31}$
$C_1 \xrightarrow{C_{M2}} C_2$	$c_1$ $HGC8I20II25I$ $c_2$	$c_1$ HGC-916II25II26I
	$c_{1+}$ $c_{2+}$ $c_{2+}$ $c_{2+}$	$c_1$
$C_1 = \frac{SW_1}{SW_2} C_2.$	C <sub>1</sub> C <sub>M2</sub> C <sub>2</sub>	C <sub>1</sub>
$C_1 \rightarrow  C_1 \rightarrow  C_2,$	$c_1$	$c_{I} \xrightarrow{C_{MI}} 1_{0} \xrightarrow{C_{2+}} C_{2+}$
	$C_1$ $C_{s2}$ $D_{s2}$ $C_2$ $HGC-141201$	C <sub>1</sub>

2. Analysis of DC-DC converter with HG cell:



Fig 1: (a) DC-DC converter with HG cell

Fig 1(a) shows the integration of high gain cell to any basic dc-dc converter. The quadratic boost converter is taken for analysis with all the cells regarding voltage gain, voltage stress and other performance related

parameters of dc-dc converter. Most of the high gain cell is made up of diode and capacitor. HGC-1-3 and 5-7 is voltage multiplier cell. "M" numbers of cells can be added to the converter to increase the voltage gain. HGC-4, 8, 9 increases the voltage conversion ratio of the converter and only single cell integration is applicable. HGC-1 to HGC-10 gives positive output voltage. HGC-11 to HGC-15 provides the negative output voltage. The theoretical voltage gain of HGC-10 is M, where M is the number of the capacitor in HG cell [27].

#### 3. Steady state analysis of Positive output Quadratic Boost converter with HG cell [PQBHG]:

To study about the positive output Quadratic boost converters in detail, few topologies are taken for analysis and their steady state analysis is carried out to determine their voltage gain with high gain cell. Fig 2 gives the Quadratic boost converter with HGC- 1cell in fig 2(a), HGC-3 in fig 2(b), HGC-4 in fig 2(c) and HGC-8 in fig 2(d).



Fig 2: Positive output Quadratic Boost Converter with High Gain Cell (a) PQBHGC-1 (b) PQBHGC-3 (c) PQBHGC-4 (d) PQBHGC-8

#### PQBHGC-1 & PQBHGC-8 Topology



Fig 3: PQBHGC-1 Topology (a) Mode 1 (b) Mode 2

Modes of operation of Positive output quadratic boost converter with HGC-1 and 4 is given fig 3(a) and (b). Single HG cell is considered for PQHGC-1 to simplify the analysis. For PQHGC-8 only single HG cell is applicable. Steady state analysis is carried out by determining the current through the inductor and voltage across the capacitor during the on and off modes.

Voltage across the inductor in mode 1 and 2 is written as

$$V_{c} = \frac{V_{g}}{[1-D]}; V_{CM1} = V_{CM2} = \frac{V_{g}}{[1-D]^{2}}$$
(4)  
The voltage conversion ratio is  

$$\frac{V_{0}}{V_{g}} = \frac{1+D}{[1-D]^{2}}$$
(5)  

$$PQBHGC-3 Topology:$$

$$(4)$$

$$V_{1} = \frac{V_{g}}{V_{1}} = \frac{V_{g}$$



Positive output quadratic boost converter derived with HG cell- 3 is a Dickson multiplier cell. For simplicity, only one cell is considered for steady state analysis and its modes of operation is given in fig 4 (a) and (b). Voltage across the inductor in mode 1 and 2 is written as

$$\begin{array}{ll} V_{L1} = V_g & ; & V_{L1} = V_g - V_C & (6) \\ V_{L2} = V_C & ; & V_{L2} = V_C - V_{CM} & (7) \\ V_{CM1} = V_{CM2} ; & V_0 = V_{CM1} + V_{CM2} & (8) \\ Applying volt second balance on Eqn 1 and 2 \\ V_c = \frac{V_g}{[1-D]}; & V_{CM1} = V_{CM2} = \frac{V_g}{[1-D]^2} & (9) \\ The voltage conversion ratio is \\ \frac{V_0}{V_g} = \frac{2}{[1-D]^2} & (10) \end{array}$$

PQBHGC-4 Topology





Fig 5: PQBHGC-4 Topology (a) Mode 1 (b) Mode 2

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Voltage across the inductor in mode 1 and 2 is written as
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$$\begin{array}{ll} V_{L1} = V_g & ; \ V_{L1} = V_g - V_C & (11) \\ V_{L2} = V_C & ; \ V_{L2} = V_C - V_{CM} & (12) \\ V_{L3} = V_{CM} & ; \ V_{L3} = 2V_{CM} - V_0 & (13) \\ Applying volt second balance on Eqn 1 and 2 \\ V_c = \underbrace{\frac{V_g}{[1-D]}}_{[1-D]}; \ V_{CM1} = V_{CM2} = \underbrace{\frac{V_g}{[1-D]^2}}_{[1-D]^2} & (14) \\ The voltage conversion ratio is \\ \frac{V_0}{V_g} = \frac{2-D}{[1-D]^2} & (15) \end{array}$$

### 4. Comparison of Positive output Quadratic Boost converter with HG cell [PQBHG]:

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The comparison of Positive output Quadratic Boost converter with HG cell [PQBHG] is carried out with following specifications: Po = 40 W, Vg = 12 V, fs = 60 kHz, D = 0.5. The information inferred from the Table 2 and 3 are given as follows:

- HGC- 1, 3 and 5 are multiplier cell, and their general voltage gain expression is given in Table 2. The gain of the converter with HGC-4, 8 and 9 cannot be increased with increase in HG cell. The voltage gain of few positive output HG cells are calculated and plotted versus duty cycle and given in Fig 6(a). HGC- 3, 5 and 9 gain is high compared to other HG cell. HGC-5 requires double the HG cell to provide the same voltage gain compared to other topologies. HGC-9 calls for an extra switch to achieve this voltage gain.
- The expression for voltage stress across the active switch is found to be similar to all the HG cell, and it depends on the input voltage and duty cycle. Switch pressure is less for HGC-3 compared to HGC-1 for different duty cycle. Maximum switch voltage versus different output voltage is given in fig 6(d).
- Switch Utilization Factor (SUF) of PQBHGC-3 is 82% of PQBHGC-1 for 96 V and 80% for 266 V. The cost of the active switch and passive switch of PQBHGC-3 is less compared to PQBHGC-1.
- 4.The rating of two inductors in positive output quadratic boost converter with the HGC-3 cell is just 88 and 75 % of the PQBHGC-1. The energy volume of the inductor of PQBHGC-3 is less compared to PQBHGC-1. Table 3 provides the comparative study of passive component rating of PQBHGC-3 and PQBHGC-1. Graphical comparison of energy volume of the inductor is given in fig 7. Therefore, the cost, rating, and volume of PQBHGC-3 are less compared to PQBHGC-1 for different voltage rating and duty cycle. Compared to several HG cell, HGC-3 is efficient compared to other HG cell.

	Voltage gain-	Voltage stress	Total components in HG cell			_	General	
Cell	QB with HG cell		Diode	Capacitor	Inductor	Multiplier cell	voltage gain expression	
PQBHGC-1	$\frac{1+D}{[1-D]^2}$		2	2	1	$\checkmark$	$\frac{\text{HG + D}}{[1 - D]^2}$	
PQBHGC-3	$\frac{2}{[1-D]^2}$		3	2	-	$\checkmark$	$\frac{\mathrm{HG}+1}{[1-\mathrm{D}]^2}$	
PQBHGC-4	$\frac{2-D}{[1-D]^2}$	- V.,	3	3	1	Х	Х	
PQBHGC-5	2 [1 – D] <sup>2</sup> (2 HG cell)	$\frac{1}{[1-D]^2}$	5	4	-	$\checkmark$	$\frac{\text{HG}}{\left[1-\text{D}\right]^2}$	
PQBHGC-8	$\frac{1+D}{[1-D]^2}$		2	2	1	Х	Х	
PQBHGC-9	$\frac{2}{[1-D]^2}$		2(1 extra switch)	2	-	Х	Х	

Topology	Inductor	Design equation of inductor	Output power = 40 W; Vg = 12 V; fs = 60 kHz				
			OUTPUT VOLATGE				
			96 V	150 V	196V	266V	
PQBHGC-1	$L_1(uH)$	$L_1 = \frac{R_0 (1 - D)^4 D}{2(1 + D)^2 f_{\infty}}$	17	19	20.4	21.4	
	$L_2(uH)$	$L_2 = \frac{R_0 (1 - D)^2 D}{2(1 + D)^2 f_s}$	86	146	198	277	
	L <sub>0</sub> (mH)	$L_{0} = \frac{R_{0}(1-D)D}{2(1+D)f_{s}}$	0.30	0.66	1.03	1.7	

Energy volume (mJ)		0.425	0.466	0.49	0.51	
PQBHGC-3 -	L <sub>1</sub> (uH)	$L_{1} = \frac{R_{0}(1 - D)^{4}D}{2(HG + 1)^{2}f}$	15	18	19.5	20.8
	L <sub>2</sub> (uH)	$L_2 = \frac{R_0 (1 - D)^2 D}{2(HG + 1)^2 f_s}$	60	112	159	232
Energy volume (mJ)			0.33	0.396	0.42	0.45



(b)



(d)

Fig 6: Performance analysis of PQBHGC (a) Voltage gain Vs Duty cycle. (b) SUF Vs Output voltage (c) Diode voltage Vs Output voltage. (d) Switch voltage Vs Output voltage.



Fig 7: Energy volume of inductor Vs Output voltage

Simulation is carried out with the nl5 simulator, and the results are presented in fig 8. Fig 8(a) and (b) provides the simulation results of PQBHGC-3. Fig 8(c) and (d) provides the simulation results of PQBHGC-1. The following information is inferred from the results obtained, and it validates the mathematical calculation of the compared topology:

• PQBHGC-1 achieves the voltage gain 8 with the duty cycle of 0.566.

- With the same voltage and power rating, the stresses across the components in PQBHGC-1 are high compared to PQBHGC-3. Voltage stress across the switch, multiplier diode, and capacitor in PQBHGC-3 is 75% of the voltage obtained in PQBHGC-1 as given in fig 8(a) and (c).
- Maximum voltage across input diode and input capacitor voltage in PQBHGC-3 is 87% of the voltage obtained in PQBHGC-1 as given in fig 8(b) and (d).
- Input inductor current waveforms are presented in fig 8(b) and (d) and ripples are found to be less in PQBHGC-3.



Fig 8: Simulation results (a), (b) Waveforms of PQBHGC-3. (c), (d) Waveforms of PQBHGC-1

#### 5. Conclusion:

This paper conducts and presents a study on High Gain (HG) cells. The voltage stress across the active switch is lessened by the HG cell. As a result, the active switch can be used with low Rds(on), which lowers the switch's conduction loss. For PQBHGC-3, simulation data are shown to demonstrate its superiority over other cells. For multiple HG cells using the quadratic boost converter, an expression representing the voltage gain and voltage stress is constructed, and it is confirmed by simulation results. These equations are used to study and analyze the performance of HG cell. Complete and systematic study on positive and negative HG cell reveals that the PQBHGC-3 has following advantage:

- 1. Higher voltage conversion ratio and it can be further increased by increasing the HG cell.
- 2. Low voltage stress and switch utilization factor which reduces the cost of the active switch.
- 3. Less energy volume of the inductor which reduces the cost and size of the converter.

Thus the PQBHGC-3 topology is the best and efficient converter to implement in stand alone or grid connected PV applications.

#### References

[1] Dongyan Zhou, Andzrej Pietkiewicz, and Slobodan ´Cuk. A Three-Switch High-Voltage Converter" IEEE Trans. Power Electron., 1999; **14(1)**: 177-183.

[2]Chung-Ming Young, Ming-Hui Chen, Tsun-An Chang, Chun-Cho Ko, et al.. Cascade Cockcroft–Walton Voltage Multiplier Applied to Transformerless High Step-Up DC–DC Converter. IEEE Transactions On Industrial Electronics, 2013; **60**(2):523-537.

[3] Yblin Janeth Acosta Alcazar, Demercil de Souza Oliveira, Jr., Fernando Lessa Tofoli, et al.. DC–DC Non isolated Boost Converter Based on the Three-State Switching Cell and Voltage Multiplier Cells. IEEE Transactions On Industrial Electronics, 2013;60(10):1382-1387.

[4]Tohid Nouri, Seyed Hossein Hosseini, Ebrahim Babaei, et al.. Generalised transformerless ultra step-up DC–DC converter with reduced voltage stress on Semiconductors. IET Power Electron., 2014; **7(11)**: 2791–2805.

[5]Xuefeng Hu and Chunying Gong. A High Gain Input-Parallel Output-Series DC/DC Converter with Dual Coupled Inductors. IEEE Trans. Power Electron., 2015; **30,(3):** -1306-1317.

[6] C. c. Lin, L. s. Yang, G. W. Wu .Study of a non-isolated bidirectional DC-DC converter. IET Power Electron., 2013; 6(1):30-37.

[7] Tohid Nouri, Seyed Hossein Hosseini, Ebrahim Babaei, Jaber Ebrahimi. Interleaved high step-up DC–DC converter based on three-winding high-frequency coupled inductor and voltage multiplier cell. IET Power Electron., 2015; **8**, (2):175–189.

[8] Yi Zhao, Wuhua Li and Xiangning He. Single-Phase Improved Active Clamp Coupled-Inductor-Based Converter with Extended Voltage Doubler Cell. IEEE Trans. Power Electron., 2012; 27, (6): 2869-2878.

[9] E.H. Ismail, M.A. Al-Saffar, A.J Sabzali. High conversion ratio DC-DC converters with reduced switch stress. IEEE Trans.Circuits Syst I Reg papers, 2008; **55(7)**: 2139-2151.

[10] M.Prudente, L. L. Pfitscher ,G. Emmendoerfer , E. F. Romaneli. Voltage Multiplier Cells Applied to Non-Isolated DC–DC Converters. IEEE Trans. Power Electron., 2008; 23,(2): 871-887

[11] Y. Berkovich, B. Axelrod , A. Shenkman," A novel diode-capacitor voltage multiplier for increasing the voltage of photovoltaic cells", Proc. IEEE COMPEL, Zurich, 2008; (12):1-5.

[12] B. Axelrod, Y. Beck, Y. Berkovich. High step-up DC–DC converter based on the switched-coupled-inductor boost converter and diodecapacitor multiplier: steady state and dynamic. IET Power Electron., 2015; 8(8):1420-1428.

[13] L. Zhang, D. Xu, G. Shen, M. Chen et al.. A High Step-Up DC to DC Converter Under Alternating Phase Shift Control for Fuel Cell Power System. IEEE Trans. Power Electron., 2015, **30**, (3), pp-1694-1703.

[14] T. Nouri ,S. H. Hosseini ,E. Babaei. Analysis of voltage and current stresses of a generalized step-up DC-DC converter. IET Power Electron., 2014;7(6):1347-1361.

[15] A. Shenkman, Y. Berkovich, B. Axelrod," Novel AC-DC and DC-DC converters with a diode-capacitor multiplier "IEEE Transactions on Aerospace and Electronic Systems 2005; **40**(4): 1286 – 1293.

[16] S. Lee, P. Kim, S. Choi. High Step-Up Soft-Switched Converters Using Voltage Multiplier Cells. IEEE Trans. Power Electron., 2015; **28**,(7): 3379 – 3387.

[17] F. L. Tofoli, D. de Souza Oliveira, R. P. Torrico-Bascopé etal. Novel Nonisolated High-Voltage Gain DC–DC Converters Based on 3SSC and VMC. IEEE Trans. Power Electron., 2015; **27**,(**8**): 3897 – 3907.

[18] M. S. Makowski. Realizability conditions and bounds on synthesis of switched-capacitor DC-DC voltage multiplier circuits. IEEE Trans. Circuits Syst. I,:Fundamental Theory and Appl ,1997:44(8): 684 – 691.

[19] E. H. Ismail, M. A. Al-Saffar , A. J. Sabzali, et al. A Family of Single-Switch PWM Converters With High Step-Up Conversion Ratio. IEEE Trans. Circuits Syst. I, Reg. Papers, 2008; **55(4)** :1159 - 1171.

[20] E. H. Ismail, M. A. Al-Saffar, A. J. Sabzali. High Conversion Ratio DC–DC Converters With Reduced Switch Stress. IEEE Trans. Circuits Syst. I, Reg. Papers, 2008; **55**(7): 2139 - 2151.

[21] Ahmad J. Sabzali, Esam H. Ismail, Hussain M. Behbehani, "High voltage step-up integrated double Boost-Sepic DC-DC converter for fuelcell and photovoltaic applications", Renewable Energy, 2015; (82): 44-53.

[22] Abbas A. Fardoun, Esam H. Ismail, Ahmad J. Sabzali etal.," Bidirectional converter for high-efficiency fuel cell powertrain", Journal of Power Sources, **249**, (2014), 470-482.

[23] Mustafa A. Al-Saffar\*, Esam H. Ismail," A high voltage ratio and low stress DC-DC converter with reduced input current ripple for fuel cell source", Renewable Energy, **82**,(2015),35-43.

[24] Tohid Nouri, Ebrahim Babaei, Seyed Hossein Hosseini," A generalized ultra step-up DC–DC converter for high voltageapplication with design considerations", Electric Power Systems Research, **105** (2013) :71–84.

[25] Wuhua Li, and Xiangning He. Review of Nonisolated High-Step-Up DC/DC Converters in Photovoltaic Grid-Connected Applications. IEEE Transactions on Industrial Electronics, 2011;**58**(4):1239-1250.

[26]Lung-Sheng Yang, Tsorng-Juu Liang, Hau-Cheng Lee, etal. Novel High Step-Up DC–DC Converter With Coupled-Inductor and Voltage-Doubler Circuits. IEEE Transactions on Industrial Electronics, 2011;58(9):777-782.

[27] Etienne Veilleux, Boon-Teck Ooi, Peter W. Lehn. Marx dc-dc converter for high-power application. IET Power Electron., 2013;6(9): 1733–1741.

[28] Indrek Roasto, Dmitri Vinnikov, Janis Zakis, etal. New Shoot-Through Control Methods for qZSI-Based DC/DC Converters. IEEE Transactions on Industrial Informatics, 2013;9(2):640-647.